

1-1-2021

## Thermal sensitivity from current-voltage-measurement temperature characteristics in Au/n-GaAs Schottky contacts

ABDULMECİT TURUT

Follow this and additional works at: <https://journals.tubitak.gov.tr/physics>



Part of the [Physics Commons](#)

---

### Recommended Citation

TURUT, ABDULMECİT (2021) "Thermal sensitivity from current-voltage-measurement temperature characteristics in Au/n-GaAs Schottky contacts," *Turkish Journal of Physics*: Vol. 45: No. 5, Article 3. <https://doi.org/10.3906/fiz-2108-15>

Available at: <https://journals.tubitak.gov.tr/physics/vol45/iss5/3>

This Article is brought to you for free and open access by TÜBİTAK Academic Journals. It has been accepted for inclusion in Turkish Journal of Physics by an authorized editor of TÜBİTAK Academic Journals. For more information, please contact [academic.publications@tubitak.gov.tr](mailto:academic.publications@tubitak.gov.tr).

## Thermal sensitivity from current-voltage-measurement temperature characteristics in Au/*n*-GaAs Schottky contacts

Abdulmecit TURUT<sup>1,\*</sup>, Hasan EFEOĞLU<sup>2</sup>

<sup>1</sup>*Faculty of Engineering and Natural Sciences, Department of Engineering Physics, Istanbul Medeniyet University, Istanbul, Turkey*

<sup>2</sup>*Faculty of Engineering, Department of Electrical and Electronics Engineering, Atatürk University, Erzurum, Turkey*

Received: 27.08.2021

Accepted/Published Online: 21.10.2021

Final Version: 02.11.2021

**Abstract:** We have measured the current–voltage–temperature (*I-V-T*) characteristics of the Au/*n*-GaAs/In Schottky barrier diodes (SBDs) to introduce their thermal sensitivity mechanism. The forward bias voltage variation with temperature (thermal sensitivity) of this SBDs has been studied at different constant current levels. The diode showed high and decisive thermal sensitivity up to a current level of 0.10 pA. The bias voltage–temperature (*V-T*) curves of the SBD have showed an excellent linear behavior at all current levels. The slope  $dV/dT = \alpha$  or the thermal sensitivity coefficient  $\alpha$  from the *V-T* curves decreased from 3.42 mV/K at 0.10 pA to 1.31 mV/K at 10 mA with increasing current level. Furthermore, the  $\alpha$  versus current graph of the diode has given a straight line from 0.10 pA to 10 mA whose intercept  $\alpha_0$  and slope  $d\alpha/dI$  values have been obtained as 2.65 mV/K and -0.081 mV/(AK). The linearity of the voltage vs temperature and the  $\alpha$  vs current graphs is a very crucial key factor of a good thermal sensor in the thermal sensitivity.

**Keywords:** Schottky diodes, temperature sensor, thermal sensitivity, thermionic emission, GaAs semiconductor

### 1. Introduction

Metal-semiconductor (MS) Schottky contacts (SCs) allow the current to flow in forward biased direction after a given specific voltage (threshold voltage) but rectify the current in the opposite reverse biased direction. The MS Schottky barrier diodes (SBDs) have a wide application field in electronic devices and in high frequency and microwave applications. The MS contacts are generally used as both ohmic and Schottky contact in electronic device fabrication [1–4]. The SCs are often used in high frequency circuits, low noise amplification, microwave and optical signal detection in circuit applications. Besides these applications, the SCs based on different Semiconductor materials were also used as a temperature sensing element in various low and high temperature applications. That is, a MS SC can also be used as a thermal sensing probe in temperature measurements [1–10]. In contrast to the *p-n* junction and semiconductor transistors, a change in the state of the SC immediately affects its parameters when a SBD is in direct contact with environment, namely gas, pressure and temperature. That is, the thermal sensing devices capable of operating under low and high temperature, high radiation, and corrosive environment provide important information about the temperature variation at the desired locations in the applications such as automotive industries, aerospace systems, industrial

\*Correspondence: [abdulmecit.turut@medeniyet.edu.tr](mailto:abdulmecit.turut@medeniyet.edu.tr)

turbines, deep-well drilling telemetric systems and cement industries [5–15]. In integrated thermal sensing units, the cryogenic temperature sensor is used as an important device [10–15].

A number of works were made on the performance of a MS SC in temperature sensing, using the variations of forward bias voltage drop across the SC with temperature [5–16]. Brezeanu et al. [5] have designed a temperature probe with a 4H-SiC SC which can sense temperature having sensitivity from 1.3 mV/K to 2.8 mV/K, over a wide temperature range from 20 °C to 400 °C. Guo et al. [7] used an Al<sub>2</sub>O<sub>3</sub> based a-IGZO (amorphous indium-gallium-zinc-oxide) SDs as a temperature sensor, and reported that the thermal sensitivity of the sensor was 0.81 mV/°C, 1.37 mV/°C and 1.59 mV/°C at the forward bias current density levels of 10<sup>-5</sup> A/cm<sup>2</sup>, 10<sup>-4</sup> A/cm<sup>2</sup> and 10<sup>-3</sup> A/cm<sup>2</sup>, respectively. Moreover, the forward voltage-temperature (*V-T*) measurements have been carried out thermally annealed and un-annealed W/*n*-GaAs SBDs by Marcano et al. [8] in the temperature range of 140K to 363 K and in the current range of 2.5 pA to 500 pA. They [8] have obtained the thermal-sensitivity coefficient ( $dV/dT = \alpha$ ) values of -2.31 mV/K and -2.59 mV/K, at 100 pA for thermally annealed and un-annealed SBDs, respectively. Filonov [9] found a  $\alpha$  value of 2 mV/°C for Pd/GaAs structures with 0.32 mm<sup>2</sup> SC area for  $I=10$  mA and  $n_0 = (1-3) \times 10^{16}$  cm<sup>-3</sup>.

Kumar et al. [10] inspected the thermal sensitivity variation trend of Ni/4H-*n*SiC temperature sensors depending on the metal contact diameter, [10] measured the thermal sensitivities in the current range from 50 pA to 1  $\mu$  A and in the temperature range of 273 K to 473 K in step of 25 K. Again, in a later article, Kumar et al. [11] introduced a study on highly sensitive and circular shaped Ni/4H-*n*SiC temperature sensors of area 3.140 mm<sup>2</sup> fabricated and characterized in temperature range of 233 K to 473 K and in forward current from 10 pA to 5 nA, and reported that the highest value of absolute thermal sensitivity for the SB diode is 3.425 mV/K at 10 pA. Benedetto et al. [12] analyzed the performances of di-vanadium Pentoxide/4H polytype of silicon carbide (V<sub>2</sub>O<sub>5</sub>/4H-SiC) temperature sensors in the temperature and current range between 147.22 and 396.75 K and 1  $\mu$  A and 1 mA, respectively. Rao et al. [13] revealed from the thermal sensing measurements of a 4H-SiC SBDs that the forward bias voltage showed a linear dependence on the temperature with the sensitivity of 1.86 mV/K at 16  $\mu$  A and 1.18 mV/K at 80  $\mu$  A.

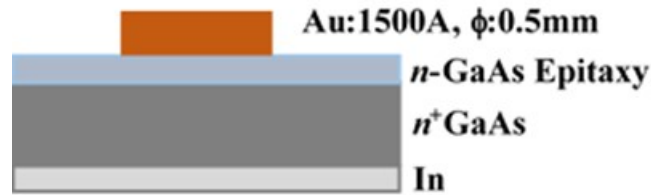
Draghici et al. [14] reported the temperature sensing element of Ni/4H-SiC SBDs with sensitivities over 2 mV/°C and excellent linearity which allows operation at temperatures up to 400 °C. Their diode temperature sensitivities were between 1.8 mV/°C and 2.54 mV/°C, for the current levels in the range 100 nA to 100  $\mu$  A [14]. Pascu et al. [15] introduced temperature-sensing element of the annealed (750 °C) and non-annealed Ni/4H-SiC SBDs over 60-700 K. The high-performance temperature sensors in dual 4H-SiC Junction BDs and SBDs were made by Min et al. [16] over 298-573 K. Their dual JBS diodes showed the higher peak sensitivity of 4.32 mV/K compared to the 2.85 mV/K of the SBD at a forward current ratio ( $I_{D2}/I_{D1}$ ) of 25 [16]. Li et al. [17, 18] fabricated the NiN/GaN and Ni/GaN SBDs, and reported from their temperature-dependent *I-V* characteristics that the NiN-SBDs have better thermal stability than that of the Ni-SBDs owing to the suppression of interface reaction between Ni and GaN, and therefore, that the thermal stability of the GaN diode with NiN (nickel nitride) anode has temperature sensing application with the sensitivity of about 1.3 mV/K. Again, in their other work, Li et al. [19] fabricated TiN/GaN SBDs with different SC diameters to investigate the temperature sensing mechanism over a temperature range of 25-200 °C, and found that the thermal sensitivity increased with the increasing SC diameter and obtained the highest sensitivity of 1.22 mV/K for SD with 300- $\mu$ m-diameter at current level of 20 mA. In addition to works above, it has been stated by Perez et al. [20] that the biased voltage

of a SBD at a given bias current level can be used as a thermos-sensitive parameter to perform temperature measurements on a monolithic integration of SBDs, and that the bias voltage versus temperature plot for a bias current of  $2.5 \text{ A/cm}^2$  had a linear variation range with a sensitivity of  $-1.6 \text{ mV/K}$  in  $300\text{--}440 \text{ K}$  [20]. Moreover, the thermal sensitivity for Ni/4H-*n*-SiC SBD was reported to vary from  $3.11 \text{ mV/K}$  at  $1 \text{ nA}$  to  $3.32 \text{ mV/K}$  at  $5 \text{ pA}$  by Kumar et al. [21].

We have measured the current-voltage ( $I$ - $V$ ) characteristics of the Au/*n*-GaAs/In SBDs in temperature range of  $350 \text{ K}$  down to  $20 \text{ K}$  by steps of  $5 \text{ K}$ . We have characterized the thermal sensitivity or cryogenic temperature behavior of the Au/*n*-GaAs/In SBDs as a temperature sensor and have operated from low current of  $0.10 \text{ pA}$  to  $10 \text{ mA}$  which have covered a broad thermal sensing range. From the applicability point of view, the thermal sensitivity study of the SBDs in low current levels is very important. Thermal-sensing applications require a strongly linear forward voltage - temperature dependence [5–15]. To the best of our knowledge, no such a study has been reported so far regarding this kind of sensitivity variation for Au/*n*-GaAs/In SBDs over a wide temperature and current range.

## 2. Experimental procedure

The Si doped *n*-type GaAs layer with  $1 \times 10^{16} \text{ cm}^{-3}$  concentration density was grown by Molecular Beam Epitaxy (MBE) in a VG VAOH system. Detailed information about the epitaxy growth on the *n*-type GaAs substrate can be found in ref. [22]. The chemically ultrasonic cleaning procedure was applied trichloroethylene, acetone and methanol for  $5 \text{ min}$  to *n*-type MBE-GaAs samples. Then, the semiconductor sample was immersed to  $5\%$  diluted HCl for  $30 \text{ sec}$ . After this step it was washed with DI water. Indium ohmic contact was evaporated at  $8 \times 10^{-7} \text{ Torr}$ , and then the *n*-type MBE-GaAs/In samples were annealed at  $450 \text{ }^\circ\text{C}$  in dry nitrogen flow for  $5 \text{ min}$  for the ohmic contact formation. Finally, Au Schottky contacts with  $0.5 \text{ mm}$  diameter were carried out in  $8 \times 10^{-7} \text{ Torr}$ . The schematic cross-section of MS Schottky diode is given in Figure 1. The  $I$ - $V$  characteristics were measured by Keithley 6515 and 2400 current-voltage source and electrometer, respectively. Cooling of samples was provided by closed cycle He cryostat. The sample temperature was measured by Lakeshore 330 temperature controller and stability was better than  $0.02 \text{ K}$  during each sampling.



**Figure 1.** Schematic cross-section of metal-semiconductor Schottky diode.

## 3. Result and discussion

It was reported that the maximum current sensitivity to temperature changes can be observed in the Schottky contacts where a thermionic emission (TE) takes place [7–11]. Therefore, we should first look at whether the current flow through the Au/*n*-GaAs/In SBDs obeys to TE model. The current transport expression with voltage through a SB diode in the standard TE model is given by [2–4]

$$I = AA^*T^2 \exp\left(-\frac{e\Phi_b(V)}{kT}\right) \left[ \exp\left(\frac{eV}{kT}\right) - 1 \right], \quad (3.1)$$

in expression above,  $A$ ,  $A^*$ ,  $V$  and  $\Phi_b(V)$  terms represent, respectively, the diode area, Richardson constant for  $n$ -type GaAs ( $8.16 \text{ Acm}^{-2}\text{K}^{-2}$ ), the forward bias voltage and the bias-dependent SBH, respectively. The  $\Phi_b(V)$  can be obtained as follows [2–4]:

$$\Phi_b(V) = \Phi_b(0) - \left(1 - \frac{1}{n}\right)V, \quad (3.2)$$

$$\beta = \left(1 - \frac{1}{n}\right), \Phi_b(V) = \Phi_b(0) - \beta V, (1 - \beta) = \frac{1}{n} \quad (3.3)$$

$$I = AA^*T^2 \exp\left(-\frac{e\Phi_b(0) + \beta eV}{kT}\right) \left[\exp\left(\frac{eV}{kT}\right) - 1\right] \quad (3.4)$$

$$I = AA^*T^2 \exp\left(-\frac{e\Phi_{b0}}{kT}\right) \exp\left(-\frac{\beta eV}{kT}\right) \left[\exp\left(\frac{eV}{kT}\right) - 1\right] \quad (3.5)$$

where  $I_0$  and  $\Phi_{b0}$  are the saturation current and zero bias SBH, and from eqn.(3.5)  $I_0$  is given by

$$I_0 = AA^*T^2 \exp\left(-\frac{\Phi_{b0}}{kT}\right). \quad (3.6)$$

From Equation (3.6),  $\Phi_{b0} = \Phi_b(0)$  is written as

$$\Phi_{b0} = \frac{kT}{q} \ln\left(\frac{I_0}{AA^*T^2}\right) \quad (3.7)$$

Thus, the TE current expression can be written as

$$\begin{aligned} I &= I_0 \exp\left(-\frac{\beta eV}{kT}\right) \exp\left(\frac{eV}{kT}\right) \left[1 - \exp\left(-\frac{eV}{kT}\right)\right] \\ I &= I_0 \exp\left(\frac{eV(1-\beta)}{kT}\right) \left[1 - \exp\left(-\frac{eV}{kT}\right)\right] \\ I &= I_0 \exp\left(\frac{eV}{nkT}\right) \left[1 - \exp\left(-\frac{eV}{kT}\right)\right], \end{aligned} \quad (3.8)$$

where

$$(1 - \beta) = \frac{1}{n}$$

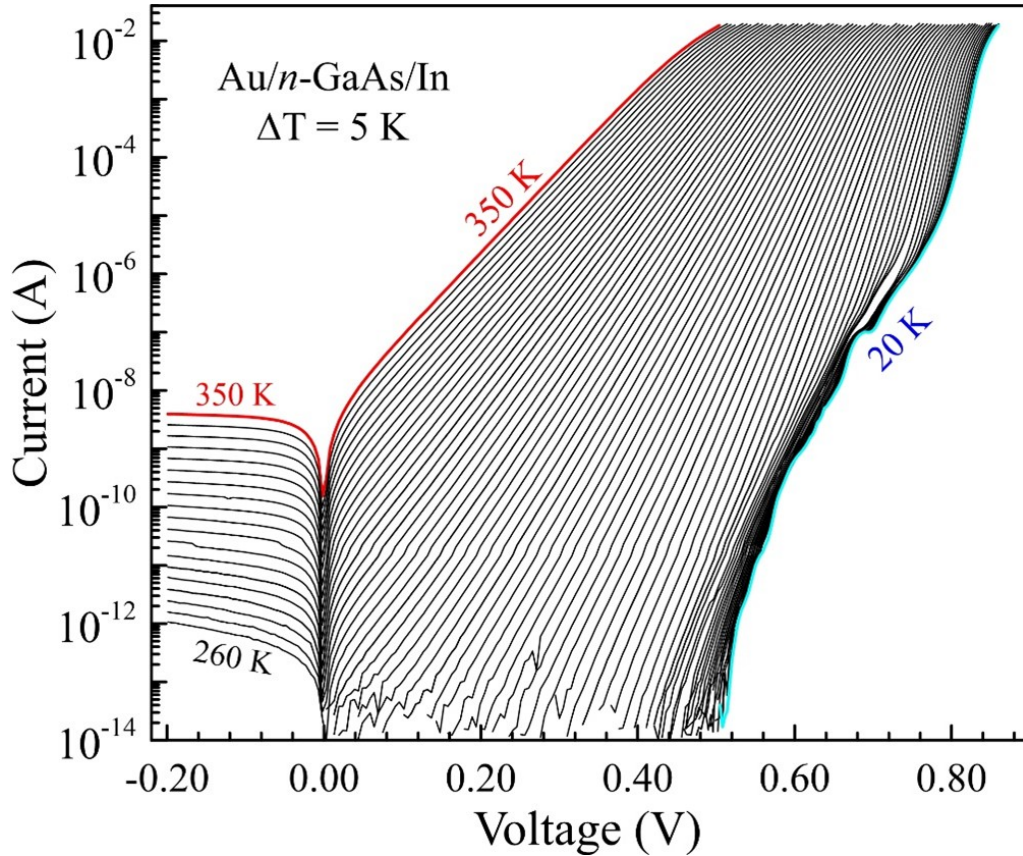
and for  $qV > 3kT$  from Equation (3.8), ideality factor  $n$  and forward bias voltage are given by

$$n = \frac{q}{kT} \frac{dV}{d(\ln I)} \quad (3.9)$$

$$V = n \frac{kT}{q} \ln\left(\frac{I}{AA^*T^2}\right) + n\Phi_{b0} \quad (3.10)$$

$$\alpha = \frac{dV}{dT} = \frac{nk}{q} \left[ \ln \left( \frac{I}{AA^*T^2} \right) - 2 \right]. \quad (3.11)$$

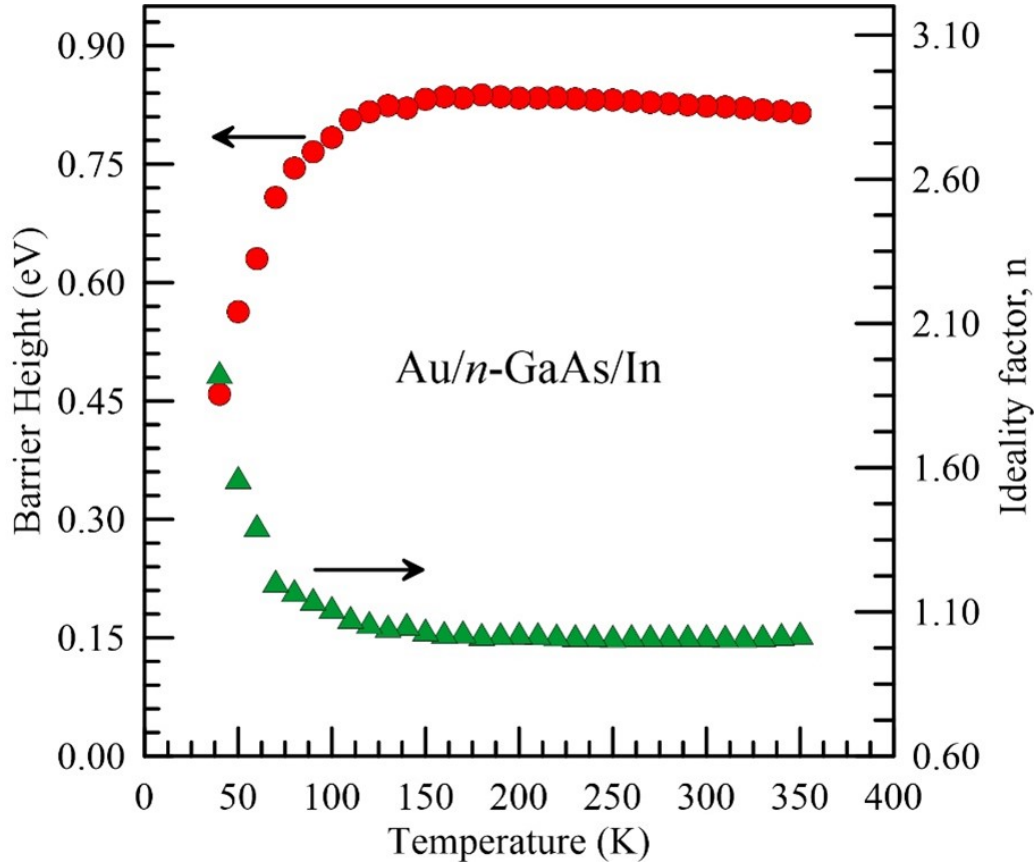
Figure 2 displays the temperature-induced  $I$ - $V$  curves of one from the Au/ $n$ -GaAs/In SBDs. It can be seen from the figure that the linear portion of the curve where TE current is dominant still occupies more than six decades in current at 350 K, with an ideality factor of 1.026. The linear part in the  $I$ - $V$  curves increases with decreasing temperature from 350 K to 120 K. It can be said that the TE fitting curves obey the experimental data quite well at high temperatures or in the high current region at low temperatures.



**Figure 2.** Dependence of the  $I$ - $V$  data on measurement temperature in the Au/ $n$ -GaAs/In diode

Some researchers [5–21] reported from the experimental thermal sensitivity studies that the maximum current sensitivity depending on temperature can be observed in the Schottky contacts where a TE takes place. Therefore, we first looked at whether the current flow through the Au/ $n$ -GaAs Schottky contact corresponds to TE. Furthermore, the bias voltage in the forward bias  $I$ - $V$  characteristics depends explicitly on barrier height and thus ideality factor. That is, the bias voltage dependent of the SBH in the intimate SBDs will also affect its thermal sensitivity. Therefore, the dependence of the bias voltage on the temperature and current in the SBDs are important factors in thermal sensitivity studies. A systematic study of the variation of these parameters with temperature is required to get a relevant explanation about measured thermal sensitivity trend in the fabricated SBD. The forward bias current flow through the diode should obey TE law and should show an ideal

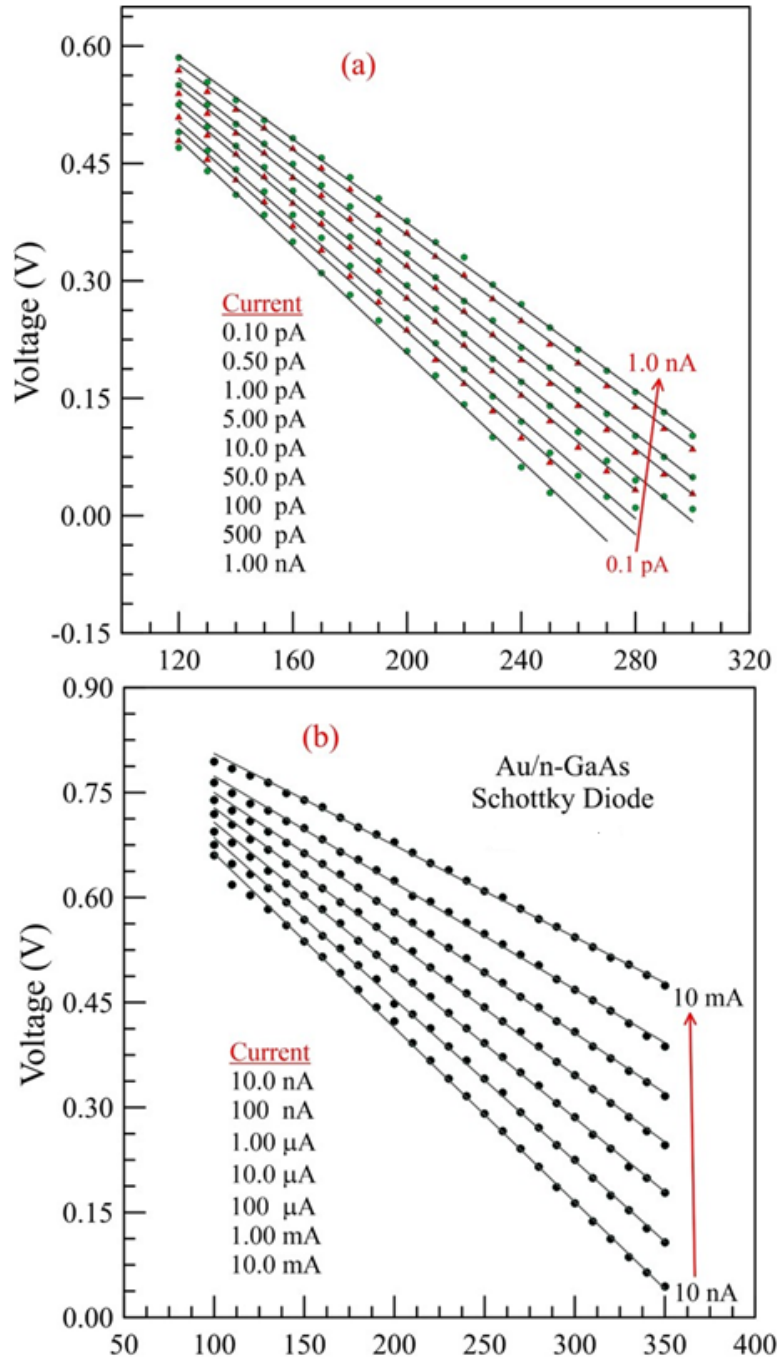
exponential type curve [22–30]. The ideality factor value ranges from 1.026 at 350 K to 1.064 at 120 K, and the values of the ideality factor close to one are evidence that the current fits the TE model [22–35].



**Figure 3.** Dependence of barrier height and ideality factor characteristics on measurement temperature in the Au/*n*-GaAs/In diode

The dependence of barrier height  $\Phi_{b0}$  and ideality factor  $n$  on measurement temperature was given in Figure 3 for the Au/*n*-GaAs/In diode. The  $\Phi_{b0}$  taken the values of 0.815 eV, 0.824 eV and 0.458 eV at 350 K, 300 K and 40 K. It can be seen from Figure 3 that the  $\Phi_{b0}$  value decreased while  $n$  value increased from 120 K down to 40 K. These low and high barrier height values are evidence that there exist inhomogeneities in barrier height formation at the Au/*n*-GaAs lateral interface. The SBH in the MS Schottky contacts has been reported to depend sharply on the interface atomic structure at epitaxial MS interfaces. In such cases, the current across the MS contact is greatly influenced by the presence of the SBH inhomogeneity [35–40]. Some researchers have considered the presence of locally non-uniform regions or patches with relatively lower or higher barriers [41–45]. Thus, they have suggested that the abnormal behaviors at low temperature can rather be adequately explained using a barrier potential fluctuation model based on spatially inhomogeneous BHs at the MS interface. Thus, it is reasonably assumed that the  $I$ - $V$  characteristics are dominated by the current flow through the low-SBH patches because the overall current across the SBD arises from low barrier height regions which are interspersed within a uniform higher barrier height region [45–56]. That is, as the temperature is decreasing, electrons lose thermal energy, and the current preferentially flows through areas with smaller barrier height with decreasing

temperature due to the BH inhomogeneity [45–56].



**Figure 4.** Dependence of the forward biased voltage on measurement temperature in Au/*n*-GaAs/In diode at different constant currents, (a) 0.10 pA to 1.0 nA, and (b) 10 nA to 10 mA

Figure 4 represents the dependence of the forward biased voltage on measurement temperature in Au/*n*-GaAs/In diode, (a) 0.10 pA to 1.0 nA, and (b) 10 nA to 10 mA. The  $V$ - $T$  curve at each current level in Figure 4 (a) and (b) given a straight line. The linearity in the thermal sensitivity plots is a key factor of a good thermal



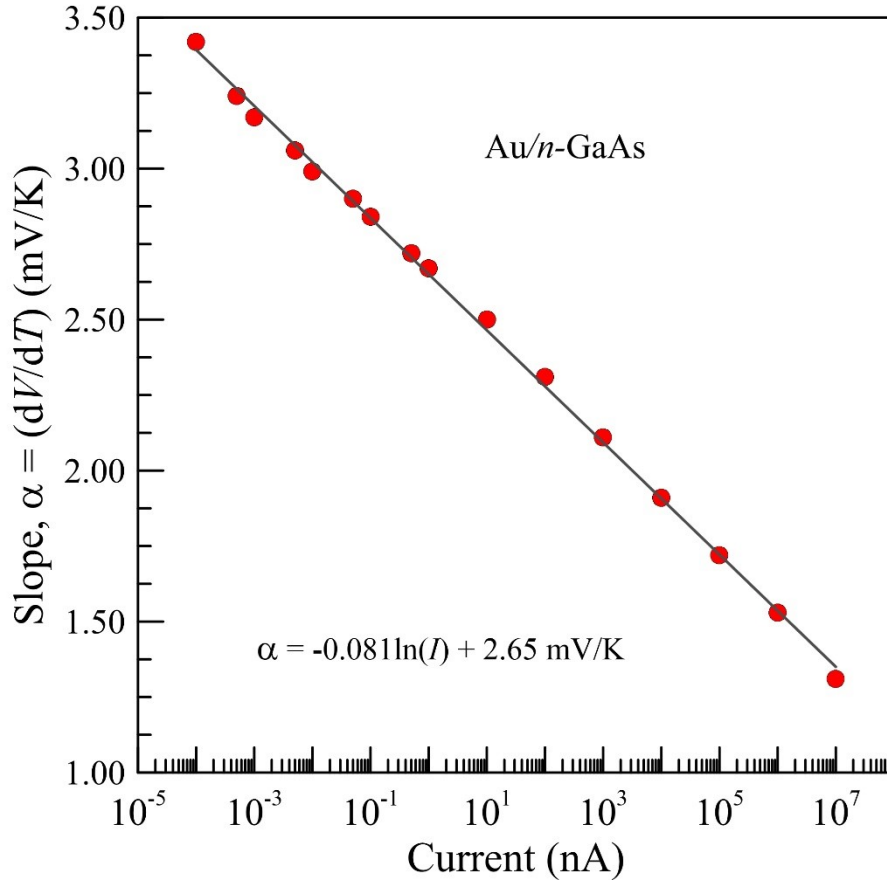
sensor. Paying attention to Figure 4 (a) and (b), the  $V$ - $T$  graph at each current level continues linearly down to approximately 120 K. The thermal sensitivity coefficient defined as the slope of  $V$ - $T$  curve depends on the  $\Phi_{b0}$  and  $n$  values. It can be noticed that these two parameters have a very low temperature dependence. The situation is clearly seen from Figure 3. The  $\Phi_{b0}$  value decreased while  $n$  value increased from 120 K down to 40 K. The series resistance of the diode causes the  $V$ - $T$  curves to deviate from the linearity at high voltages. We can state that the series resistance restricts the linearity of the  $V$ - $T$  characteristics for higher bias current level. This is more evident at higher current levels in Figure 4 (b), for example, at 0.75 V for the current level of 10 mA. The thermal sensitivity coefficient values  $\alpha = (dV/dT)$  corresponding to constant current values for the Au/ $n$ -GaAs/In SBD are given in Table 1. The slope  $dV/dT = \alpha$  or the thermal sensitivity coefficient  $\alpha$  from  $V$ - $T$  curves increased with decreasing current level and it ranged from 3.42 mV/K at 0.10 pA to 1.31 mV/K at 10 mA. Furthermore, we have also obtained a value of  $\alpha = 2.67$  mV/K at the current level of 1.0 nA.

**Table 1.** The thermal sensitivity coefficients,  $\alpha = (dV/dT)$ , corresponding to constant current values in the Au/ $n$ -GaAs/In SBD. These values have been obtained from the  $V$ - $T$  curves in Figure 4 (a) and (b).

<i>Current</i>	$(dV/dT)(\text{mV/K})$
0.10 pA	3.42
0.50 pA	3.24
1.00 pA	3.17
5.00 pA	3.06
10.0 pA	2.99
50.0 pA	2.90
100 pA	2.84
500 pA	2.72
1.00 nA	2.67
10.0 nA	2.50
100 nA	2.31
1.00 $\mu\text{A}$	2.11
10.0 $\mu\text{A}$	1.91
100 $\mu\text{A}$	1.72
1.00 mA	1.53
10.0 mA	1.31

It has been seen that the sensitivity for SiC SBDs [5] has varied from 1.3 mV/K from 0.10 mA to 2.8 mV/K at 0.10 nA with the forward current in 300 K to 400 K range. Marcano et al. [8] obtained  $\alpha$  values of 2.31 mV/K and 2.59 mV/K, at 100 pA for thermally 750 °C annealed and un-annealed W/ $n$ -GaAs SBDs, respectively, in the measurement temperature range of 140-363 K. Filonov [9] reported a thermal sensitivity coefficient of  $\alpha = 2$  mV/°C at  $I=10$  mA for Pd/GaAs structures.

The thermal sensitivity  $\alpha$  versus forward current plot of the diode is given in Figure 5. The thermal



**Figure 5.** Dependence of the thermal sensitivity  $\alpha$  on current level in the Au/n-GaAs/In diode

sensitivity of the Au/n-GaAs/In diode as a function of forward current level is expressed theoretically by Equation (3.11). That is, the thermal sensitivity has followed Equation (3.11) because the current across the diode obeys the TE model down to about 120 K. The experimental data in the graph have been fitted Equation (3.11). It has been clearly depicted that thermal sensitivities linearly have varied with logarithmic value of the current. The intercept and slope values of the straight line in Figure 5 have been obtained as 2.65 mV/K and -0.081 mV/(AK) for the diode, respectively.

#### 4. Conclusion

It has been seen that the current flow through the fabricated Schottky contact obeys to TE model with the ideality factor values ranging from 1.026 at 350 K to 1.064 at 120 K. This is an important property in the experimental thermal sensitivity studies for the fact that the maximum current sensitivity depending on temperature can be observed in the Schottky contacts. The forward biased voltage versus measurement temperature curve at each current level for the Au/n-GaAs/In diode has given a straight line in the current range of 0.10 pA to 10 mA. The linearity in the thermal sensitivity is a key factor of a good thermal sensor. The thermal sensitivity coefficient  $\alpha$  from  $V$ - $T$  curves increased with decreasing current level and ranged from 3.42 mV/K at 0.10 pA to 1.31 mV/K at 10 mA. Furthermore, we have also obtained a value of  $\alpha = 2.67$  mV/K

at the current level of 1.0 nA.

## References

- [1] S. Demirezen, H.G. Cetinkaya, M. Kara, F. Yakuphanoglu and S. Altındal, Synthesis, electrical and photo-sensing characteristics of the Al/(PCBM/NiO: ZnO)/p-Si nanocomposite structure, *Sensors and Actuators A: Physical* **317**, 112449 (2021). doi: 10.1016/j.sna.2020.112449
- [2] A. Türüt, On current-voltage and capacitance-voltage characteristics of metal-semiconductor contacts, *Turkish Journal of Physics* **44**, 302–347 (2020). doi: 10.3906/fiz-2007-11
- [3] E.H. Rhoderick, Metal-semiconductor contacts, *E. H. IEE Proceeding, Solid State and Electron Devices* **129** (1982). doi: 10.1049/ip-i-1.1982.0001
- [4] M.S. Gorji and K. Y. Cheong, Embedded nanoparticles in schottky and ohmic contacts: A review, *Crit. Rev. Solid State Mater Sci.* **40**, 197-222 (2015). doi: 10.1080/10408436.2014.940444
- [5] G. Brezeanu, F. Draghici, F. Craciunioiu, C. Boianceanu, F. Bernea, F. Udrea, D. Puscasu and I. Rusu, 4H-SiC Schottky diodes for temperature sensing applications in harsh environments, *Materials Science Forum* **679–680**, 575–578 (2011). doi: 10.4028/www.scientific.net/MSF.679-680.575
- [6] G. Pristavu, G. Brezeanu, R. Pascu, F. Drăghici and M. Bădilă, Characterization of non-uniform Ni/4H-SiC Schottky diodes for improved responsivity in high-temperature sensing, *Materials Science in Semiconductor Processing* **94**, 64–69, (2019). doi: 10.1016/j.mssp.2019.01.018
- [7] Q. Guo, F. Lu, Q. Tan, T. Zhou, J. Xiong and W. Zhang, Al<sub>2</sub>O<sub>3</sub> -based a-IGZO Schottky Diodes for temperature sensing, *Sensors* **19**, 224 (2019). doi: 10.3390/s19020224.
- [8] N. Marcano, A. Singh and F. Perez, Voltage-temperature characteristics of W/n-GaAs Schottky diodes activated by the constant forward current: application as temperature sensors, *Proceedings of the second IEEE International Caracas Conference on Devices, Circuits and Systems (Cat. No.98TH8350)*, 88-91 (1998). doi: 10.1109/IC-CDCS.1998.705812.
- [9] N.G. Filonov, A Stable Temperature Sensor Based on GaAs Structures with Schottky Barriers, *Instruments and Experimental Techniques* **45**, 412–415, (2002). doi: 10.1023/A:1016088110238.
- [10] V. Kumar, S. Pawar, A.S. Maan and J. Akhtar, Diameter dependent thermal sensitivity variation trend in Ni/4H-SiC Schottky diode temperature sensors, *Journal of Vacuum Science & Technology B* **33**, 052207 (2015). doi: 10.1116/1.4929890
- [11] V. Kumar, J. Verma, A.S. Maan and J. Akhtar, Epitaxial 4H-SiC based Schottky diode temperature sensors in ultra-low current range, *Vacuum* **182**, 109590, (2020). doi: 10.1016/j.vacuum.2020.109590.
- [12] L. Di Benedetto, G.D. Licciardo, S. Rao, G. Pangallo, F.G. Della Corte and A. Rubino, V<sub>2</sub>O<sub>5</sub>/4H-SiC Schottky Diode Temperature Sensor: Experiments and Model, in *IEEE Transactions on Electron Devices* **65**, 687-694 (2018). doi: 10.1109/TED.2017.2785234.
- [13] S. Rao, L. Di Benedetto, G. Pangallo, A. Rubino, S. Bellone and F.G. Della Corte, 85-440 K Temperature Sensor Based on a 4H-SiC Schottky Diode, *IEEE Sensors Journal* **16**, 6537–6542, (2016). doi: 10.1109/JSEN.2016.2591067.
- [14] F. Draghici, G. Brezeanu, G. Pristavu and R. Pascu, 400 °C Sensor Based on Ni/4H-SiC Schottky Diode for Reliable Temperature Monitoring in Industrial Environments, *Sensors* **19**, 2384, (2019). doi: 10.3390/s19102384.
- [15] R. Pascu, G. Pristavu, G. Brezeanu, F. Draghici, P. Godignon, C. Romanitan, M. Serbanescu and A. Tulbure, 60–700 K CTAT and PTAT Temperature Sensors with 4H-SiC Schottky Diodes, *Sensors* **21**, 942, (2021). doi: 10.3390/s21030942
- [16] S.J. Min, M.C. Shin, N.T. Nguyen, J.M. Oh and S.M. Koo, High-performance temperature sensors based on dual 4H-SiC JBS and SBD devices, *Materials* **13**, 1–8 (2020). doi: 10.3390/ma13020445

- [17] L. Li, J. Chen, X. Gu, X. Li, T. Pu and J.P. Ao, Temperature sensor using thermally stable TiN anode GaN Schottky barrier diode for high power device application, *Superlattices and Microstructures* **123**, 274–279 (2018). doi: 10.1016/j.spmi.2018.09.007
- [18] X. Li, T. Hoshi, L. Li, T. Pu, T. Zhang, T. Xie, X. Li, J.P. Ao, GaN Schottky barrier diode with thermally stable nickel nitride electrode deposited by reactive sputtering, *Materials Science in Semiconductor Processing* **93**, 1–5 (2019). doi: 10.1016/j.mssp.2018.12.018
- [19] X. Li, T. Pu, X. Li, L. Li and J.P. Ao, Correlation between Anode Area and Sensitivity for the TiN/GaN Schottky Barrier Diode Temperature Sensor, *IEEE Transactions on Electron Devices* **67**, 1171–1175 (2020). doi: 10.1109/TED.2020.2968358
- [20] G. Perez, G. Chicot, Y. Avenas, P. Lefranc, P.O. Jeannin, D. Eon and N. Rouger, Integrated temperature sensor with diamond Schottky diodes using a thermosensitive parameter, *Diamond and Related Materials* **78**, 83–87 (2017). doi: 10.1016/j.diamond.2017.08.008
- [21] V. Kumar, A.S. Maan and J. Akhtar, Barrier height inhomogeneities induced anomaly in thermal sensitivity of Ni/4H-SiC Schottky diode temperature sensor, *J Vac Sci Technol. B, Nanotechnol Microelectron Mater Process Meas Phenom.* **32**, 041203 (2014). doi: 10.1116/1.4884756
- [22] M. Missous, E.H. Rhoderick and K.E. Singer, Thermal stability of epitaxial Al/GaAs Schottky barriers prepared by molecular-beam epitaxy, *J. Appl. Phys.* **59**, 3189-3195 (1986). doi: 10.1063/1.336900
- [23] S. Zhu, X. Qu and B. Li, Schottky barrier characteristics of ternary silicide  $\text{Co}_{1-x}\text{Ni}_x\text{Si}_2$  on n-Si(100) contacts formed by solid phase reaction of multilayer, *Solid State Electronics* **48**, 1205–1209 (2004). doi: 10.1016/j.sse.2004.02.006
- [24] A.F. Özdemir, T. Göksu, N. Yıldırım and A. Turut, Effects of measurement temperature and metal thickness on Schottky diode characteristics, *Physica B: Condensed Matter* **616**, 413125 (2021). doi: 10.1016/j.physb.2021.413125
- [25] M. Biber, Ö. Güllü, S. Forment, R.L. Van Meirhaeghe and A. Türüt, The effect of Schottky metal thickness on barrier height inhomogeneity in identically prepared Au/n-GaAs Schottky diodes, *Semiconductor Science and Technology* **21**, 1–5 (2006). doi: 10.1088/0268-1242/21/1/001
- [26] V.R. Reddy and C.J. Choi, Microstructural and interface properties of Au/SrTiO<sub>3</sub> (STO)/n-GaN heterojunction with an e-beam evaporated high-k STO interlayer, *Journal of Alloys and Compounds* **823**, 153775 (2020). doi: 10.1016/j.jallcom.2020.153775
- [27] A. Turut, D.E. Yıldız, A. Karabulut and I. Orak, Electrical characteristics of atomic layer deposited Au/Ti/HfO<sub>2</sub>/n-GaAs MIS diodes in the wide temperature range, *J Mater Sci Mater Electron* **31**, 7839-7849 (2020). doi: 10.1007/s10854-020-03322-w
- [28] A.R. Deniz, The analyzing of I-V performance of PbO<sub>2</sub>/n-Si heterojunction in the wide temperature range, *Journal of Alloys and Compounds* **888**, 161523 (2021). doi: 10.1016/j.jallcom.2021.161523
- [29] Z.J. Horváth, Semiconductor nanocrystals in dielectrics: optoelectronic and memory applications of related silicon-based MIS devices, *Current Applied Physics* **6**, 145-148 (2006). doi: 10.1016/j.cap.2005.07.028
- [30] J. Osvald, T. Lalinski and G. Vanko, High temperature current transport in gate oxides based (GaN)/AlGaIn/GaN Schottky diodes, *Applied Surface Science* **461**, 206-211 (2018). doi: 10.1016/j.apsusc.2018.06.113
- [31] R.T. Tung, From NiSi<sub>2</sub> experiments to density functional theory calculations: How the Schottky barrier mystery was solved, *J. Vac. Sci. Technol. A* **39**, 020803, (2021). doi: 10.1116/6.0000689
- [32] E. Dobročka and J. Osvald, Influence of barrier height distribution on the parameters of Schottky diodes, *Applied Physics Letter* **65**, 575-577 (1994). doi: 10.1063/1.112300
- [33] S. Chand and J. Kumar, On the existence of a distribution of barrier heights in Pd<sub>2</sub>Si/Si Schottky diodes, *Journal of Applied Physics* **80**, 288-294 (1996). doi: 10.1063/1.362818

- [34] A.F. Hamida, Z. Ouennoughi, A. Sellai, R. Weiss and H. Ryssel, Barrier inhomogeneities of tungsten Schottky diodes on 4H-SiC, *Semiconductor Science and Technology* **23**, 6 (2008). doi: 10.1088/0268-1242/23/4/045005
- [35] B. Abay, G. Çankaya, H.S. Güder, H. Efeoglu and Y. K. Yoğurtçu, Barrier characteristics of Cd/p-GaTe Schottky diodes based on I-V-T measurements, *Semiconductor Science and Technology* **18**, 75-81 (2003). doi: 10.1088/0268-1242/18/2/302
- [36] A. Kumar, K.K. Sharma, S. Chand and A. Kumar, Investigation of barrier inhomogeneities in I-V and C-V characteristics of Ni/n-TiO<sub>2</sub>/p-Si/Al heterostructure in wide temperature range, *Superlattices and Microstructures* **122**, 304-315 (2018). doi: 10.1016/j.spmi.2018.07.034
- [37] A. Baltakesmez, S. Tekmen and B. Güzeldir, Temperature dependent current- and capacitance-voltage characteristics of W/n-Si structures with two-dimensional WS<sub>2</sub> and three-dimensional WO<sub>3</sub> interfaces deposited by RF sputtering technique, *Materials Science in Semiconductor Processing* **118**, 105204 (2020). doi: 10.1016/j.mssp.2020.105204
- [38] P.G. McCafferty, A. Sellai, P. Dawson and H. Elabd, Barrier characteristics of PtSi/p-Si Schottky diodes as determined from I-V-T measurements, *Solid-State Electronics* **39**, 583-592 (1996). doi: 10.1016/0038-1101(95)00162-X
- [39] O.F. Yüksel, N. Tugluoğlu, H. Şafak, Z. Nalçacıgil, M. Kuş *et al.*, Analysis of temperature dependent electrical properties of Au/perylene-diimide/n-Si Schottky diodes, *Thin Solid Films* **534**,614-620 (2013). doi: 10.1016/j.tsf.2013.02.042
- [40] M. Keskin M, A. Akkaya, E. Ayyıldız, A. Uygun Öksüz and M. Özbay Karakuş, Investigation of the temperature-dependent electrical properties of Au/PEDOT:WO<sub>3</sub>/p-Si hybrid device, *Journal of Materials Science Electron* **30**, 16676-16686 (2019). doi: 10.1007/s10854-019-02048-8
- [41] R.T. Tung, Electron transport at metal-semiconductor interfaces: general theory, *Physical Review B* **45**, 13509-13523 (1992). doi: 10.1103/PhysRevB.45.13509
- [42] Ö.S. Aniltürk and R. Turan, Electrical transport at a non-ideal CrSi<sub>2</sub>-Si junction, *Solid-State Electron* **44**, 41-48 (2000). doi: 10.1016/S0038-1101(99)00204-X
- [43] Z.J. Horváth, E. Ayyıldız, V. Rakovics, H. Cetin and B. Pödör, Schottky contacts to InP, *Physica Status Solidi C* **2**, 1423-1427 (2005). doi: 10.1002/pssc.200460479
- [44] F. Iucolano, F. Roccaforte, F. Giannazzo and V. Raineri, Barrier inhomogeneity and electrical properties of PtGaN Schottky contacts, *Journal of Applied Physics* **102**, 1-8 (2007). doi: 10.1063/1.2817647
- [45] N. Kavasoglu, A.S. Kavasoglu and B. Metin, A different approach to solar cell simulation, *Materials Research Bulletin* **70**, 804-808 (2015). doi: 10.1016/j.materresbull.2015.06.007
- [46] Ç. Güçlü, A.F. Özdemir and Ş. Altındal, Double exponential I-V characteristics and double Gaussian distribution of barrier heights in (Au/Ti)/Al<sub>2</sub>O<sub>3</sub>/n-GaAs (MIS)-type Schottky barrier diodes in wide temperature range, *Applied Physics A* **122**, 1032 (2016). doi: 10.1007/s00339-016-0558-x
- [47] T. Tuğç, Ş. Altındal, I. Uslu, L. Dökme and H. Uslu, Temperature dependent current-voltage (I-V) characteristics of Au/n-Si (111) Schottky barrier diodes with PVA(Ni,Zn-doped) interfacial layer, *Mater. Sci. Semicond. Process.* **14**, 139-145 (2011). doi: 10.1016/j.mssp.2011.01.018
- [48] P.R. Sekhar Reddy, V. Janardhanam, K.H. Shim *et al.*, Temperature dependent Schottky barrier characteristics of Al/n-type Si Schottky barrier diode with Au-Cu phthalocyanine interlayer, *Thin Solid Films* **713**, 138343 (2020). doi: 10.1016/j.tsf.2020.138343
- [49] W. Huang, T. Lin, C. Horng and Y. Li, *Materials Science in Semiconductor Processing* The electrical characteristics of Ni / n-GaSb Schottky diode, *Mater. Sci. Semicond. Process.* **16**, 418-423 (2013). doi: 10.1016/j.mssp.2012.08.011

- [50] S. Duman, B. Gürbulak, S. Dogan and A. Türüt, Electrical characteristics and inhomogeneous barrier analysis of Au-Be/p-InSe: Cd Schottky barrier diodes, *Microelectronic Engineering* **86**, 106-110 (2009). doi: 10.1016/j.mee.2008.10.004
- [51] B. Sürücü B, H.H. Güllü, M. Terlemezoğlu, D.E. Yıldız and M. Parlak, Determination of current transport characteristics in Au-Cu/CuO/n-Si Schottky diodes, *Physica B: Condensed Matter* **579**, 246-253 (2019). doi: 10.1016/j.physb.2019.06.024
- [52] A. Tataroğlu and Ş. Altındal, The distribution of barrier heights in MIS type Schottky diodes from current-voltage-temperature (I-V-T) measurements, *Journal of Alloys and Compounds* **479**, 893-897 (2009). doi: 10.1016/j.jallcom.2009.01.098
- [53] H. Durmuş, H. Ş. Kılıç, S.Y. Gezin and Ş. Karataş, Analysis of current-voltage-temperature and capacitance-voltage temperature characteristics of Re/n-Si Schottky contacts, *Silicon* **10**, 361-369 (2018). doi: 10.1007/s12633-016-9456-2
- [54] A. Akkaya, L. Esmer, T. Karaaslan, H. Çetin and E. Ayyıldız, Electrical characterization of NiAl<sub>0.09</sub>Ga<sub>0.91</sub>N Schottky barrier, *Materials Science in Semiconductor Processing* **28**, 127-134 (2014). doi: 10.1016/j.mssp.2014.07.053
- [55] M. Sağlam, B. Güzeldir, A. Türüt and D. Ekinçi, Role of Reduced Graphene Oxide-Gold Nanoparticle Composites on Au/Au-RGO/p-Si/Al Structure Depending on Sample Temperature, *J. Electron Mater* **50**, 4752-4761 (2021). doi: 10.1007/s11664-021-09017-0
- [56] H. Helal, Z. Benamara, M. Ben Arbia, A. Rabehi, A.C. Chaouche and H. Maaref, Electrical behavior of n-GaAs based Schottky diode for different contacts: Temperature dependence of current-voltage, *Int. J. Numer. Model Electron Networks, Devices Fields* **34**, e2916 (2021). doi: 10.1002/jnm.2916